



ELECTRONICS

USER 'S MANUAL ERRATA

This document contains the corrections of errors, typos and omissions in the following document.

***Samsung 8-bit CMOS S3C921F/P921F
Microcontroller User's Manual***

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1. ELECTRICAL DATA (PAGE 17-3)**Table 17-2. D.C. Electrical Characteristics** $(T_A = -40^\circ\text{C to } +85^\circ\text{C}, V_{DD} = 2.4\text{ V to } 5.5\text{ V})$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input High voltage	V_{IH1}	Ports 1-4	$0.8 V_{DD}$	-	V_{DD}	V
	V_{IH2}	RESET	$0.8 V_{DD}$		V_{DD}	
	V_{IH3}	X_{IN} , X_{OUT} and XT_{IN}	$V_{DD} - 0.1$		V_{DD}	

2. ELECTRICAL DATA (PAGE 17-5)**Table 17-2. D.C. Electrical Characteristics (Continued)** $(T_A = -40^\circ\text{C to } +85^\circ\text{C (except } I_{DD5}, V_{DD}=5\text{ V } \pm 10\%), V_{DD} = 2.4\text{ V to } 5.5\text{ V})$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit	
Supply current	I_{DD5}	Stop mode; $V_{DD} = 5\text{ V } \pm 10\%$, $T_A = 25^\circ\text{C}$	OSCCON.2="1"	-	0.3	3	μA
		$V_{DD} = 3\text{ V } \pm 10\%$,			0.1	1	

3. ELECTRICAL DATA (PAGE 17-12)**Table 17-9. VLD Electrical Characteristics** $(T_A = -40^\circ\text{C to } +85^\circ\text{C}, V_{DD} = 2.4\text{ V to } 5.5\text{ V})$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
VLD Voltage	V_{VLD}	BLDCON.4 = 0B	2.4	2.7	3.0	V
		BLDCON.4 = 1B	3.6	4.0	4.4	
VLD Circuit Response Time	TB	$f_w = 32.768\text{ kHz}$	-	-	1.0	mS
VLD Operating Current	IBL		-	50	100	μA